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Application Number	10599215
Filing Date	2006-09-22
First Named Inventor	RADULESCU
Art Unit	
Examiner Name	
Attorney Docket Number	NL040302

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1	XU, JUN: "ASYNCHRONOUS INTERCONNECTION AND INTERFACING OF INTELLECTUAL PROPERTY CORES IN THE DESIGN OF SYSTEMS-ON-CHIP"; CHAPTERS 5 & 6; 'ONLINE'; 1 JULY 2002; CENTRE FOR CONCURRENT SYSTEMS AND VLSI, SCHOOL OF COMPUTING INFORMATION SYSTEMS AND MATHEMATICS, SOUTH BANK UNIVERSITY; XP00233385.	<input type="checkbox"/>
2	RADULESCU A. ET AL: "AN EFFICIENT ON-CHIP NETWORK INTERFACE OFFERING GUARANTEED SERVICES, SHARED-MEMORY ABSTRACTION, AND FLEXIBLE NETWORK CONFIGURATION"; PROCEEDINGS OF THE DESIGN, AUTOMATION AND TEST IN EUROPE CONFERENCE AND EXHIBITION, 2004; PROCEEDINGS FEB. 16-20, 2004, PISCATAWAY, NJ, USA, IEEE 16 FEBRUARY 2004, PP.878-883, XP010684780.	<input type="checkbox"/>

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